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PATENT ABSTRACTS OF JAPAN

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(57) Abstract:

CONSTITUTION: The transmitter 100 is provided with a memory 103 having an area storing a fixed code and a code number sequentially revised every time the transmitter 100 is operated and a transmission circuit 101 generating a revision number through the prescribed arithmetic operation to the fixed number, generating a revision code number through a prescribed arithmetic operation with respect to the code number and outputting the generated revision number and the revision code number to a reception circuit 202.

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